K & S partners
Intellectual Property Attorneys

NAVEEN SURIYA

Partner - Computer & Electronics, Design

IP advisor to 25+ Leading Technology Corporations

Naveen manages the entire patent lifecycle from filing to prosecution in the areas of electronics, communications, medical devices, mechatronics, semiconductors, industrial automation, computer engineering, and industrial designs.

QUALIFICATIONS

- LL.B
- M.Tech in Computer Networks
- B.E in Electronics & Communication
- One-year Diploma in Electronics & Communication
- Advanced Diploma in Software Engineering – NIIT
- Microsoft Certified in Developing Applications with C++ using the Microsoft Foundation Class Library

Registered Patent Agent
Admitted to the Bar

PRACTICE AREAS

- Electronics & Communication Engineering

EXPERTISE

- Digital Signal Processing
- Sensors
- Robotics
- Microcontrollers
- 4G/5G Communication
- Image Processing Techniques
- Control Systems

ACTIVE IP EVANGELIST

Naveen is a regular speaker at various conferences and seminars. He also conducts IP awareness workshops and training programs for R&D teams of corporates.

ROBUST OPERATIONAL EXPERIENCE

Naveen regularly appears before the Indian Patent Office in connection with prosecution and oppositions of patent applications.

PREVIOUS WORK EXPERIENCE

3+ years as Lecturer at Vema Institute of Technology (under VTU)

20+ Years Experience | **30+** Subject Expertise | **5K** Patents Managed

MEMBERSHIPS

LES INDIA · APAA Asian Patent Attorneys Association · ASSOCHAM · FICCI · IACC Indo-American Chamber of Commerce · CII · ICC International Chamber of Commerce The world business organization · KSBC · Swiss-Indian Chamber of Commerce · NASSCOM · ICCK Indian Chamber of Commerce in Korea

PROGRAMMING LANGUAGES

- Assembly Level Languages - 8085, 8086 (TASM), and 8051
- Programming Languages - C, C++
- Operating Systems - RedHat, Linux, Win-9x/2k, and MS-DOS
- Stimulators ORCAD, PSPICE, VHDL, and MATLAB
- Knowledgeable in computer network concepts and digital logic design

INTERNSHIP

9 months as a project trainee at National Aerospace Laboratories (NAL)

ACADEMIC PROJECTS

- MPI implementation of NetCDF (Module 2) at Flosolver (National Aerospace Laboratories)
- Simulation of Doppler Weather RADAR signals & implementation of clutter filter using TMS320C54xx at ISRO Telemetry Tracking and Command Network (ISTRAC)
- Digital Code Lock at B.T.L Institute of Technology
- Synchronous Triggering of SCR Firing Angle using PC at SGR Polytechnic
- Video Library at NIIT
- Inventory Control at NIIT

EXTRA CURRICULAR ACTIVITIES

- Reading books
- Playing Football (represented college team)
- Listening to music

LANGUAGES KNOWN

English, Kannada, Hindi, Telugu, & Tamil